

# Optimizing Implementations of Linear Layers

Zejun Xiang, Xiangyong Zeng, Da Lin, Zhenzhen Bao, Shasha Zhang

Nov. 09, 2020

# Lightweight Cryptography

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## -Requirements

- ◆ Circuit size

- ◆ Latency

- ◆ Throughput

- ◆ .....

## -Lightweight Components:

- ◆ Lightweight MDS or near-MDS matrix

- ◆ Sbox?

## -Primitives:

- ◆ SIMON, SPECK

- ◆ PRESENT, RECTANGLE

- ◆ LED, MIDORI

- ◆ .....

## -Optimization (sbox)

- ◆ Gladman (Serpent)

- ◆ SAT based method (Stoffelen, FSE 2016)

- ◆ LIGHTER (Jean, FSE 2017)

- ◆ .....

# Optimization---Linear layer

## Local optimization

- ❑ optimize the multiplication (finite field or matrix-vector multiplication) of each entry of a matrix.
- ❑ **Method:** using different basis  
reuse intermediate values  
LIGHTER
- ❑ **Cost** = fixed cost + multiplication
- ❑ **AES** = 96 + ()

## Global optimization

- ❑ treat the linear layer as a binary matrix and find the minimal number of xor operations to implement the matrix
- ❑ **Method:** Paar1 and Paar2  
BP (Boyar and Peralta)  
LIGHTER  
variants.....
- ❑ **Cost** = returned by the algorithms
- ❑ **AES** = 92

# Metrics

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Consider a matrix to be implemented,  $M = \begin{pmatrix} 1 & 1 & 1 & 0 \\ 1 & 1 & 0 & 1 \\ 1 & 0 & 1 & 0 \\ 0 & 1 & 1 & 0 \end{pmatrix}$

$$GF(2): \begin{cases} y_1 = x_1 \oplus x_2 \oplus x_3 \\ y_2 = x_1 \oplus x_2 \oplus x_4 \\ y_3 = x_1 \oplus x_3 \\ y_4 = x_2 \oplus x_3 \end{cases}$$

D-xor

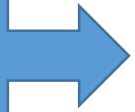


Counting the number of 1's within a binary matrix.

$$D\text{-xor}(M) = 6$$

# Metrics

G-xor



Counting the minimal number of operations  $x_i = x_m \oplus x_n$  implementing the matrix.

Shortest Linear Straight-Line Program, NP-hard

$$t_1 = x_1 \oplus x_3 \quad (y_3)$$

$$t_2 = x_2 \oplus x_3 \quad (y_4)$$

$$t_3 = x_1 \oplus x_2$$

$$t_4 = t_3 \oplus x_3 \quad (y_1)$$

$$t_5 = t_3 \oplus x_4 \quad (y_2)$$

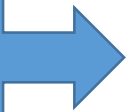
D-xor = 6



G-xor = 5

# Metrics

S-xor



Counting the nominal number of operations  $x_i = x_i \oplus x_j$  implementing the matrix.

Optimal pivoting in Gauss-Jordan elimination

$$\begin{array}{ll} x_4 = x_4 \oplus x_2 & x_4 \oplus x_2 \\ x_4 = x_4 \oplus x_1 & x_4 \oplus x_2 \oplus x_1 = y_2 \\ x_1 = x_1 \oplus x_3 & x_1 \oplus x_3 = y_3 \\ x_3 = x_3 \oplus x_2 & x_3 \oplus x_2 = y_4 \\ x_2 = x_2 \oplus x_1 & x_2 \oplus x_1 \oplus x_3 = y_1 \end{array}$$

S-xor = 5

# Extra advantage of S-xor

$$\begin{aligned}t_1 &= x_1 \oplus x_3 & (y_3) \\t_2 &= x_2 \oplus x_3 & (y_4) \\t_3 &= x_1 \oplus x_2 \\t_4 &= t_3 \oplus x_3 & (y_1) \\t_5 &= t_3 \oplus x_4 & (y_2)\end{aligned}$$

Bit-sliced software  
implementation

$$\begin{aligned}t_1 &= x_1 \wedge x_3 & (y_3) \\t_2 &= x_2 \wedge x_3 & (y_4) \\t_3 &= x_1 \wedge x_2 \\t_4 &= t_3 \wedge x_3 & (y_1) \\t_5 &= t_3 \wedge x_4 & (y_2)\end{aligned}$$

$$\begin{aligned}t_1 &= x_1 \wedge x_3 & (y_3) \\t_2 &= x_2 \wedge x_3 & (y_4) \\x_1 &= x_1 \wedge x_2 \\x_3 &= x_1 \wedge x_3 & (y_1) \\x_4 &= x_1 \wedge x_4 & (y_2)\end{aligned}$$

$$t_1 = x_1 \wedge x_3$$

```
movl x1, t1
xorl x3, t1
```

Xor  
destructive

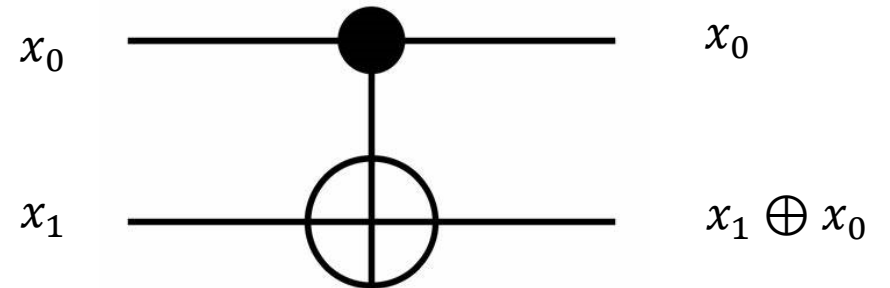
# Extra advantage of S-xor

$$\begin{array}{ll} x_4 = x_4 \wedge x_2 & x_4 \oplus x_2 \\ x_4 = x_4 \wedge x_1 & x_4 \oplus x_2 \oplus x_1 = y_2 \\ x_1 = x_1 \wedge x_3 & x_1 \oplus x_3 = y_3 \\ x_3 = x_3 \wedge x_2 & x_3 \oplus x_2 = y_4 \\ x_2 = x_2 \wedge x_1 & x_2 \oplus x_1 \oplus x_3 = y_1 \end{array}$$



```
xorl x2, x4
xorl x1, x4
xorl x3, x1
xorl x2, x3
xorl x1, x2
```

Quantum Implementation



CNOT gate



# Elementary operation and elementary matrix

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Type-1

Interchange two rows (columns)

$$E(1 \leftrightarrow 2) = \begin{pmatrix} 0 & 1 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 1 \end{pmatrix}$$

Type-2

Multiply a row (column) with a nonzero number

$$E(1 + 2 * k) = \begin{pmatrix} 1 & 0 & 0 \\ 0 & k & 0 \\ 0 & 0 & 1 \end{pmatrix}$$

Type-3

Add a row (column) to another one multiplied by a nonzero number

$$E(1 + 2 * k) = \begin{pmatrix} 1 & k & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{pmatrix}$$

# Cost of elementary matrix

Type-1  $\rightarrow E(1 \leftrightarrow 2) = \begin{pmatrix} 0 & 1 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 1 \end{pmatrix}$

Type-3  $\rightarrow E(1 + 2) = \begin{pmatrix} 1 & 1 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{pmatrix}$

$$\begin{pmatrix} 0 & 1 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} x_1 \\ x_2 \\ x_3 \end{pmatrix} = \begin{pmatrix} y_1 = x_2 \\ y_2 = x_1 \\ y_3 = x_3 \end{pmatrix}$$

Cost = 0

$$\begin{pmatrix} 1 & 1 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} x_1 \\ x_2 \\ x_3 \end{pmatrix} = \begin{pmatrix} y_1 = x_1 \oplus x_2 \\ y_2 = x_2 \\ y_3 = x_3 \end{pmatrix}$$

Cost = 1 (S-xor)

# Matrix Decomposition

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Theorem

*Any invertible matrix can be transformed into an identity matrix using elementary row and/or column operations. Thus, any invertible matrix can be decomposed as a product of elementary matrices.*



Corollary

*Any matrix in  $GL(2, F_2)$  can be transformed into an identity matrix by applying a series of type-1 and type-3 elementary row and/or column operations. Thus, any matrix in  $GL(2, F_2)$  can be decomposed as a product of type-1 and type-3 elementary matrices.*

# Matrix decompositions

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1

Elementary row operation based matrix decomposition

**Gaussian Elimination**

2

Elementary column operation based matrix decomposition

3

Hybrid elementary operation based matrix decomposition

Pick the elementary operation which minimize the most number of 1's in the given matrix

Tie break: the first one VS random

Increase the number of 1's  $\rightarrow$  infinite loop  $\rightarrow$  1 or 2

# Matrix Decomposition

Property

$$E(i + j)E(k \leftrightarrow l) = E(k \leftrightarrow l)E\left(f_{k,l}(i) + f_{k,l}(j)\right),$$
$$E(k \leftrightarrow l)E(i + j) = E\left(f_{k,l}(i) + f_{k,l}(j)\right)E(k \leftrightarrow l),$$

where

$$f_{k,l}(x) = \begin{cases} k, & \text{if } x = l, \\ l, & \text{if } x = k, \\ x, & \text{else.} \end{cases}$$

Theorem

Any matrix  $M$  in  $GL(2, F_2)$  can be decomposed as:  
 $M = E(i_t + j_t) \cdots E(i_1 + j_1)E(i'_s \leftrightarrow j'_s) \cdots E(i'_1 \leftrightarrow j'_1)$



$$\text{Cost}(M) = t$$

# Properties of matrix multiplication

## Property

*Let  $E(i \leftrightarrow j)$  and  $E(i+j)$  denote a type-1 and type-3 elementary matrices in  $GL(2, F_2)$  respectively, then the following equations hold.*

$$\text{R1} \quad E(k+i)E(k+j)E(i+j) = E(i+j)E(k+i)$$

$$\text{R2} \quad E(i+k)E(k+j)E(i+j) = E(k+j)E(i+k)$$

$$\text{R3} \quad E(i+k)E(j+k)E(i+j) = E(i+j)E(j+k)$$

$$\text{R4} \quad E(j+k)E(i+k)E(i+j) = E(i+j)E(j+k)$$

$$\text{R5} \quad E(k+j)E(k+i)E(i+j) = E(i+j)E(k+i)$$

$$\text{R6} \quad E(k+j)E(i+k)E(i+j) = E(i+j)E(k+j)$$

$$\text{R7} \quad E(j+i)E(i+j) = E(i \leftrightarrow j)E(j+i)$$

# Properties of matrix multiplication

$$E(k+i)E(k+j)E(i+j) = E(i+j)E(k+i)$$

Consider  $E(k+i)E(k+j)E(i+j)x$ , where  $x = (x_1, x_2, \dots, x_n)$ .  
Only  $x_i, x_j$  and  $x_k$  are involved in the computation.

$x_i$	$x_j$	$x_k$
$x_i \oplus x_j$	$x_j$	$x_k$
$x_i \oplus x_j$	$x_j$	$x_k \oplus x_j$
$x_i \oplus x_j$	$x_j$	$x_k \oplus x_i$

equivalent

$x_i$	$x_j$	$x_k$
$x_i$	$x_j$	$x_k \oplus x_i$
$x_i \oplus x_j$	$x_j$	$x_k \oplus x_i$

# Properties of matrix multiplication



matrix multiplication does NOT generally satisfy the commutative law.

special cases

Property

*Let  $i, j, k, l$  be integers and  $i \neq j \neq k \neq l$ , then we have*

1  $E(k + l)E(i + j) = E(i + j)E(k + l)$

2  $E(i + j)E(k + j) = E(k + j)E(i + j)$

3  $E(i + j)E(i + k) = E(i + k)E(i + j)$



# Example

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cost = 4

$$M = E(3 + 2)E(3 + 4)E(3 + 1)E(2 + 1)$$

commutative

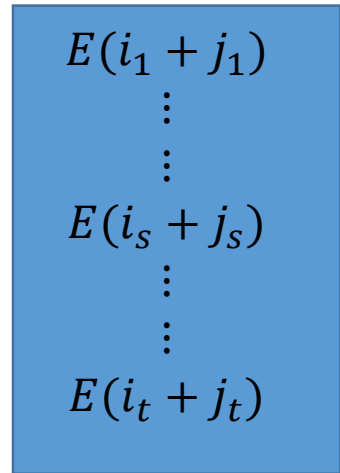
$$M = E(3 + 4)E(3 + 2)E(3 + 1)E(2 + 1)$$

reduction (R1)

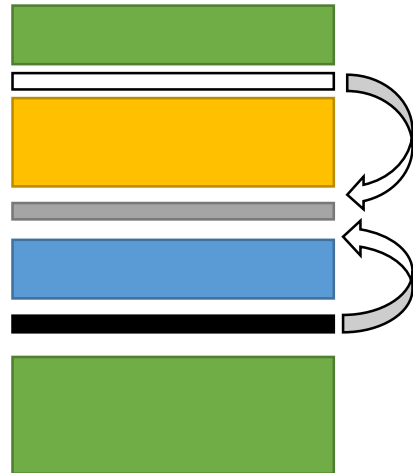
cost = 3

$$M = E(3 + 4)E(2 + 1)E(3 + 2)$$

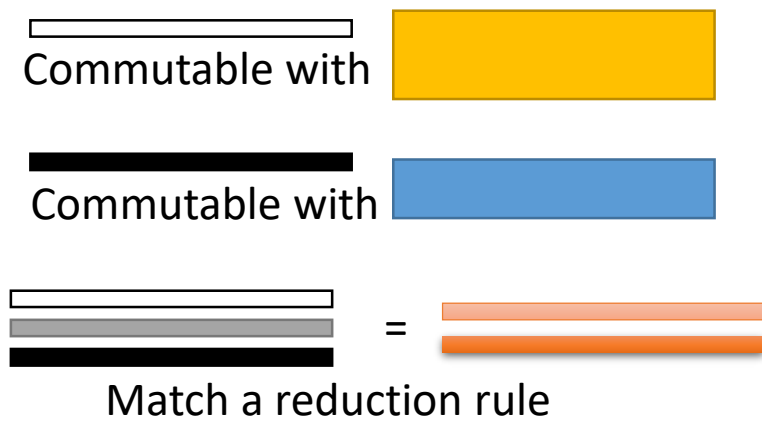
# Reduction algorithm



A given matrix decomposition



Identify possible reduction

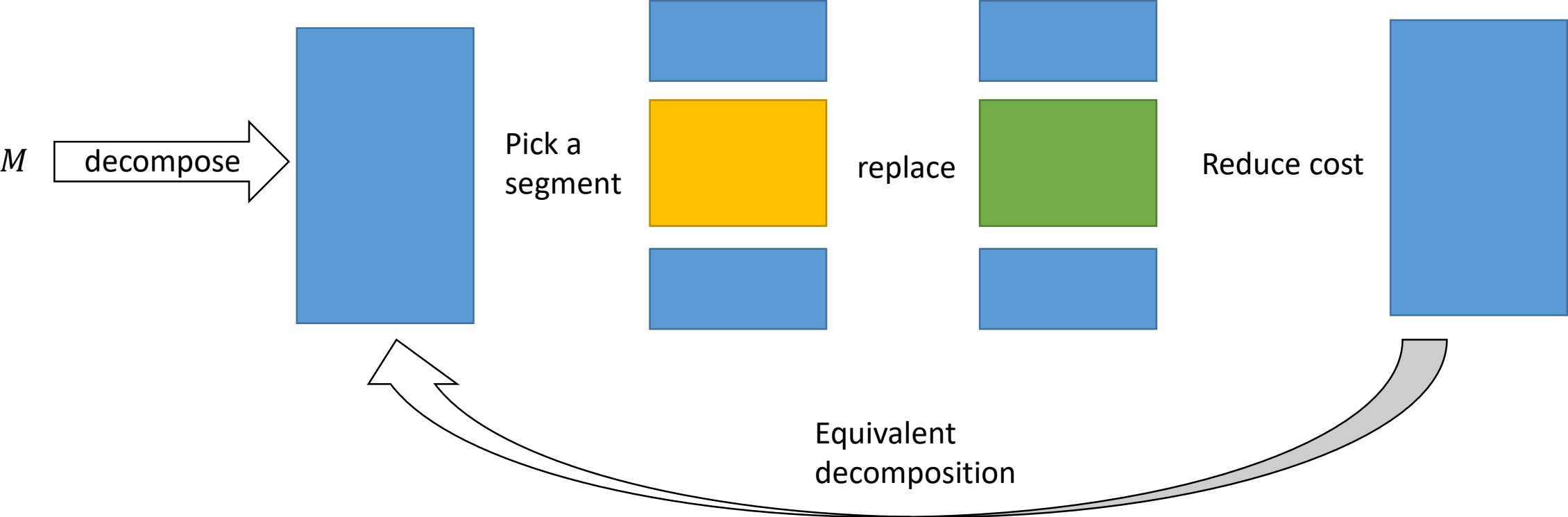


Conditions



Reduction

# Search algorithm



# Applications

**Table 1:** Implementation cost of cipher matrices under different optimization heuristics.

Cipher	Size	[KLSW17] <sup>1</sup>	[KLSW17] <sup>2</sup>	[KLSW17] <sup>3</sup>	[BFI19]	This paper
FOX MU8 [JV04]	64	611	-	594	592	-
GROSTL [GKM <sup>+</sup> 09]	64	493	-	475	460	-
KHAZAD [BR00b]	64	488	-	507	492	366
WHIRLPOOL [BR00c]	64	481	-	465	464	-
AES <sup>4</sup> [DR02]	32	108	108	97	95	92
ANUBIS [BR00a]	32	122	121	113	102	99
CLEFIA $M_0$ [SSA <sup>+</sup> 07]	32	121	121	106	102	98
CLEFIA $M_1$ [SSA <sup>+</sup> 07]	32	121	121	111	110	103
FOX MU4 [JV04]	32	144	143	137	131	136
TWOFISH [SKW <sup>+</sup> 98]	32	151	149	129	125	111
JOLTIK [JNP14]	16	52	48	48	47	44
SMALLSCALE AES [CMR05]	16	54	54	47	45	43
WHIRLWIND $M_0$ [BNN <sup>+</sup> 10]	32	218	218	212	210	183
WHIRLWIND $M_1$ [BNN <sup>+</sup> 10]	32	246	244	235	234	190
QARMA128 [Ava17]	32	48	48	48	48	48
MIDORI [BBI <sup>+</sup> 15]	16	24	24	24	24	24
PRINCE $M_0, M_1$ [BCG <sup>+</sup> 12]	16	24	24	24	24	24
PRIDE $L_0 - L_3$ [ADK <sup>+</sup> 14]	16	24	24	24	24	24
QARMA64 [Ava17]	16	24	24	24	24	24
SKINNY64 [BJK <sup>+</sup> 16]	16	12	12	12	12	12

<sup>1</sup> This column presents the results in [KLSW17] obtained by Paar1's heuristic.

<sup>2</sup> This column presents the results in [KLSW17] obtained by Paar2's heuristic.

<sup>3</sup> This column presents the results in [KLSW17] obtained by Boyar-Peralta's heuristic.

<sup>4</sup> [Max19] presents a 92-XOR implementation of AES.

# On Inverse Matrices

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$$M = E_1 E_2 \cdots E_n \quad M^{-1} = E_n^{-1} E_{n-1}^{-1} \cdots E_1^{-1}$$



the cost of the inverse of  $M$  = the cost of  $M$



The inverse of AES MixColumns can be implemented using 92 xor's.

**Thanks!**