

Amsterdam, The Netherlands, 9-12th September, 2018

ExpFault: Automated Framework for Exploitable Fault Characterization in Block Ciphers

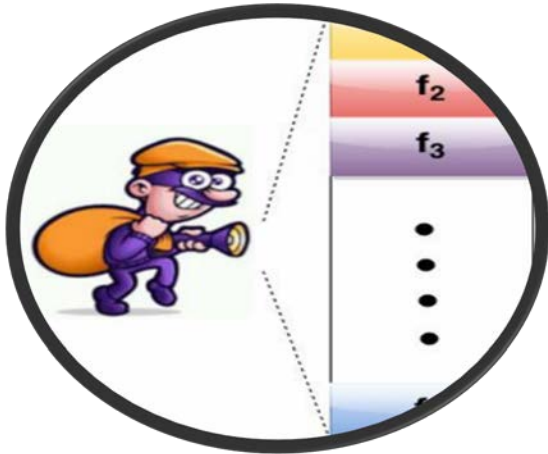
Sayandeep Saha, Debdeep Mukhopadhyay, and Pallab Dasgupta



Motivation



Testing Block Ciphers for Fault Attacks



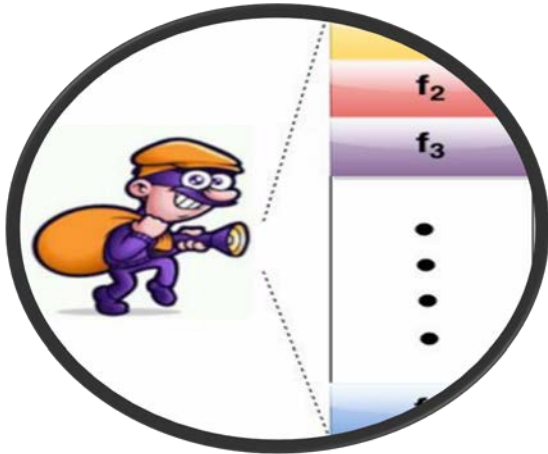
Securing cryptographic devices against fault attacks

- Several countermeasures exist :
 - Either extremely resource hungry
 - Or not robust against all possible faults
- Malicious faults are highly repeatable
- Require precise idea of the *fault model*
 - ***Which faults do we need to prevent?***

Motivation



Testing Block Ciphers for Fault Attacks



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“Exploits” of Exploitable Faults

- Designing precise countermeasures
- Testing countermeasures
 - On “non-random” exploitable fault space.
- Cipher evaluation

Motivation



Challenges

Not all faults are malicious/exploitable

- Example of malicious fault: 8th/ 9th round byte faults in AES
- Example of non-malicious fault: 5th round byte fault in AES

Fault space for a block cipher:

- Prohibitively large !!!
- Manual fault analysis methodologies -- Impractical !!!
- **It took 8 years to reach the optimal attack for AES**

Too many ciphers !!!

- Trend of designing application-specific lightweight ciphers
- **Recent NIST call for lightweight block ciphers.**

Motivation



Challenges

Not all faults are malicious/exploitable

- F
-

An automation:

- **Generic**
- **Fast**
- **Scalable.**

Fault

Too many ciphers

- Trend of designing application-specific lightweight ciphers
- **Recent NIST call for lightweight block ciphers.**

NIST call for lightweight cryptography (<http://nvlpubs.nist.gov/nistpubs/ir/2017/NIST.IR.8114.pdf>)

Fault Attack Automation



State-of-the-art

Algebraic Fault Attack (AFA)

- Generic representation.
- Use of SAT solvers.
- Not so fast !!!
- Lack of interpretability.

F. Zhang et.al. , “A Framework for the Analysis and Evaluation of Algebraic Fault Attacks on Lightweight Block Ciphers”, *IEEE Transactions on Information Forensics and Security*, 11(5), 1039-1054., 2016

Synthesis of Fault attacks

- Program synthesis based
- Demonstrated on Public key systems

Gilles Barthe, et al. "Synthesis of fault attacks on cryptographic implementations." *Proceedings of the 2014 ACM SIGSAC Conference on Computer and Communications Security.*, 2014.

Proposed Approach

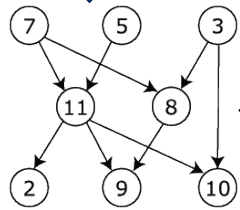
- **What DFA does?**
 - Reduces the key search space with faults
 - Exhaustive search within practical limits
- **What we suggest ...**
 - Do not perform the exhaustive search
 - Automatically compute the search complexity
- **Advantage**
 - Fast characterization of each individual faults
 - Challenge: Generic algorithm.

Proposed Approach: ExpFault



Cipher Description

Attack Complexity
Attack Procedure
Required Number of injections



Modeled Fault

$$F = \langle s_r^{i_1}, \lambda, wd, t \rangle$$

CDG Analysis

Exploitable?

Data Mining

Fault Properties



What we have obtained so far...

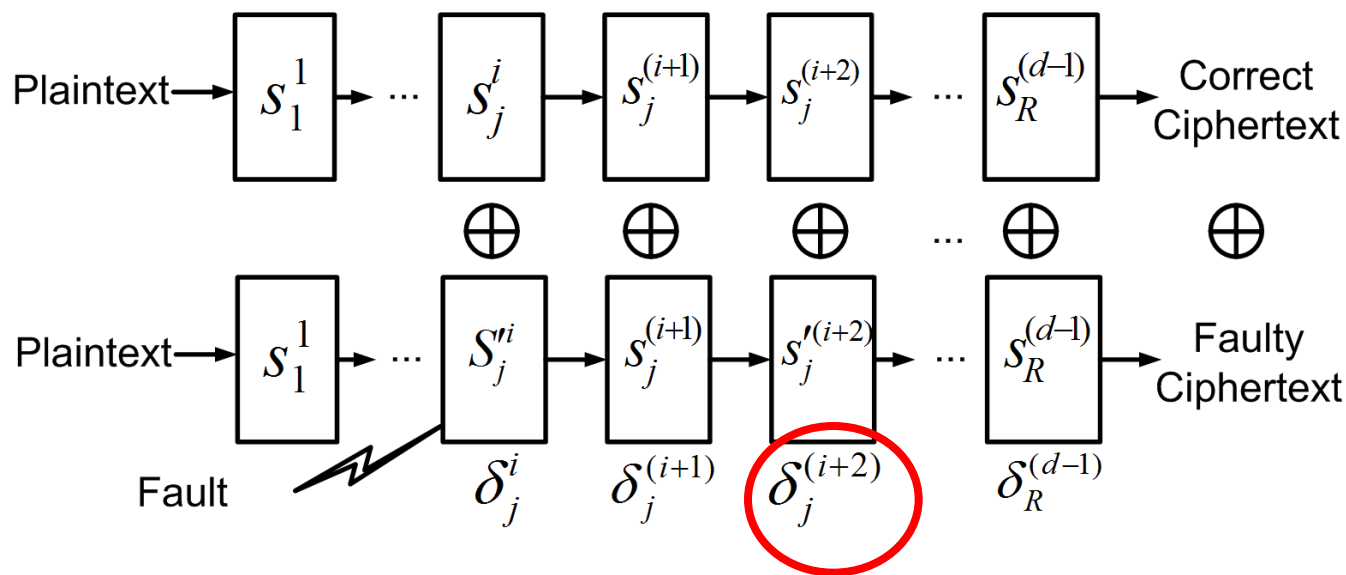


Block Cipher	Fault Location	Fault Model	Remain key space complexity	Number of Injections Required	Optimal?
AES	8 th round SBox	byte	2^8	1	Yes
AES	7 th round SBox	byte	1	2043	—
PRESENT	28 th round SBox	Multi-byte	1	2	Yes
GIFT	25 th and 23 rd round SBox	nibble	$2^{7.06}$	2	Yes

The attacks on GIFT were not previously known!!!!

Proposed Approach

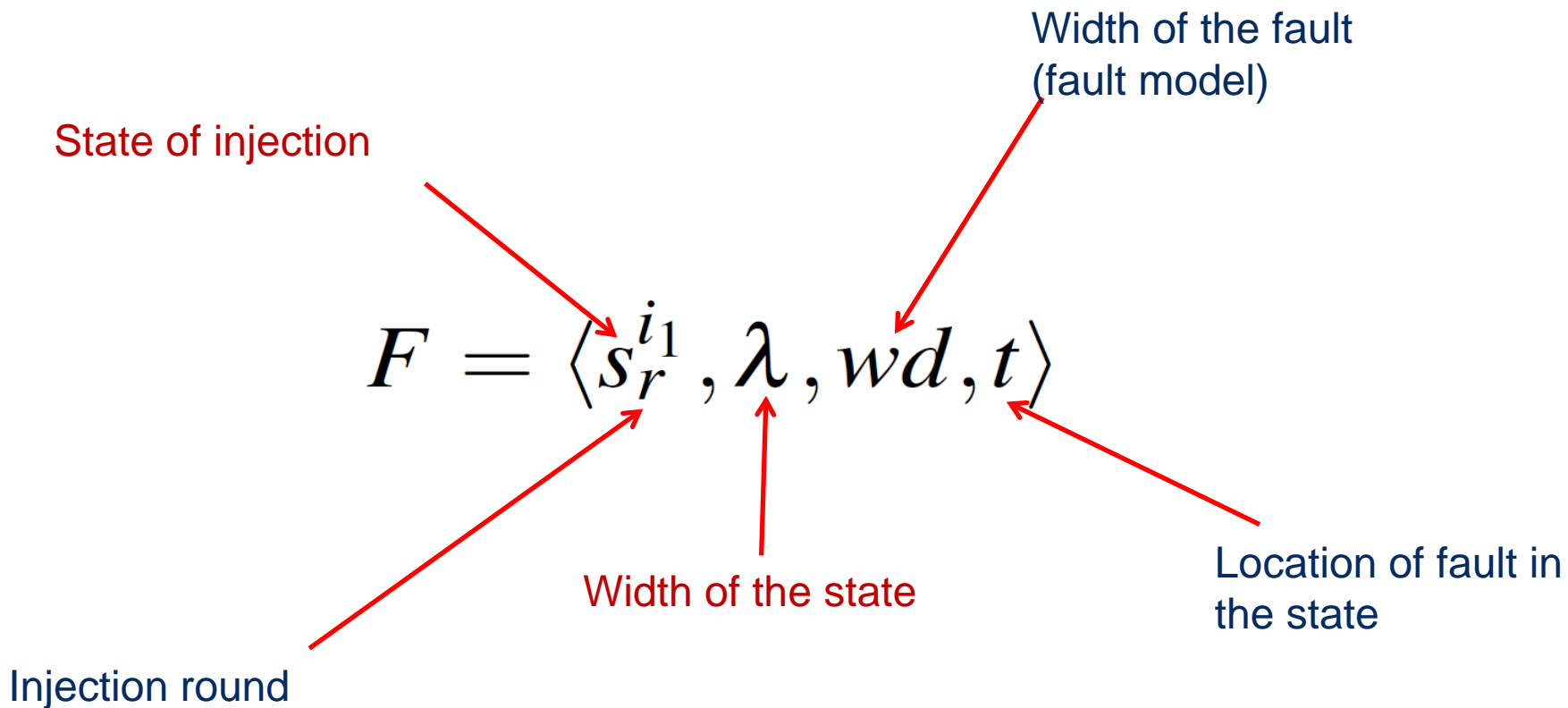
How to Represent a Cipher



We analyze the dataset for each “state-differential” δ_j^i

Proposed Approach

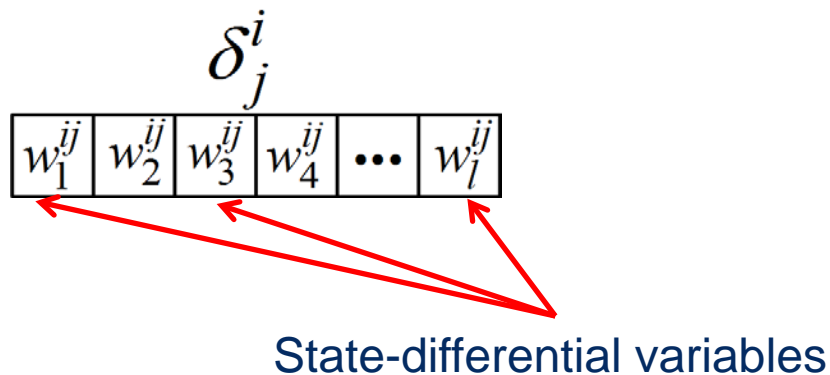
How to Represent a Fault



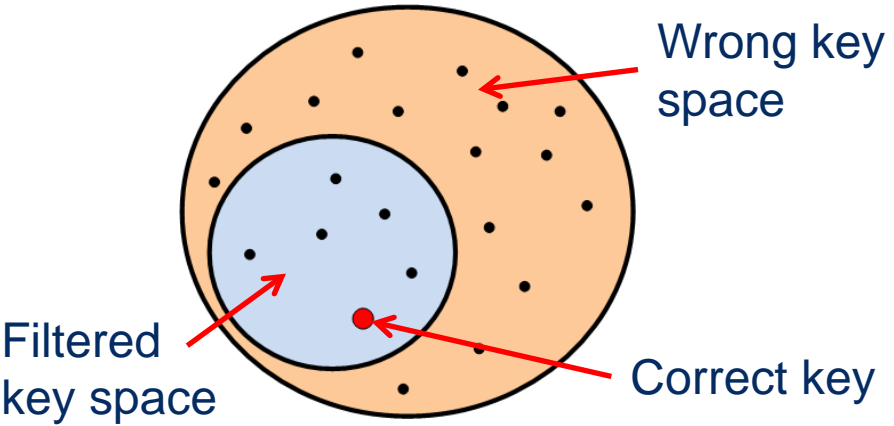
Fault and plaintext values are abstracted out

Proposed Approach

What is a “Distinguisher”?

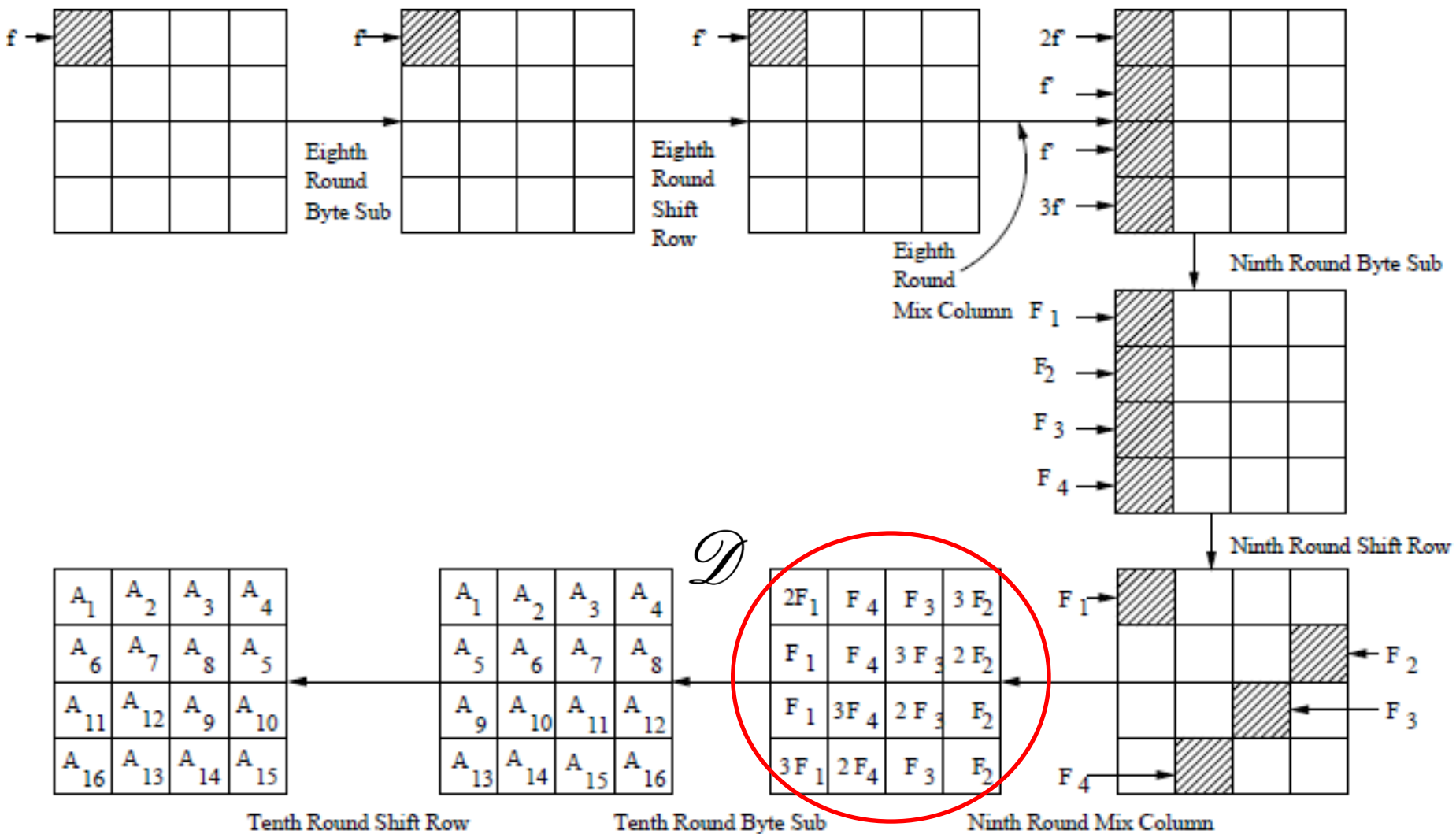


- Constraints over state-differential variables
- Satisfiable for the correct key and few others
- Results in non-uniform nature of the state-differential distribution for a “small” subset of keys
- Works as “filter” for wrong keys

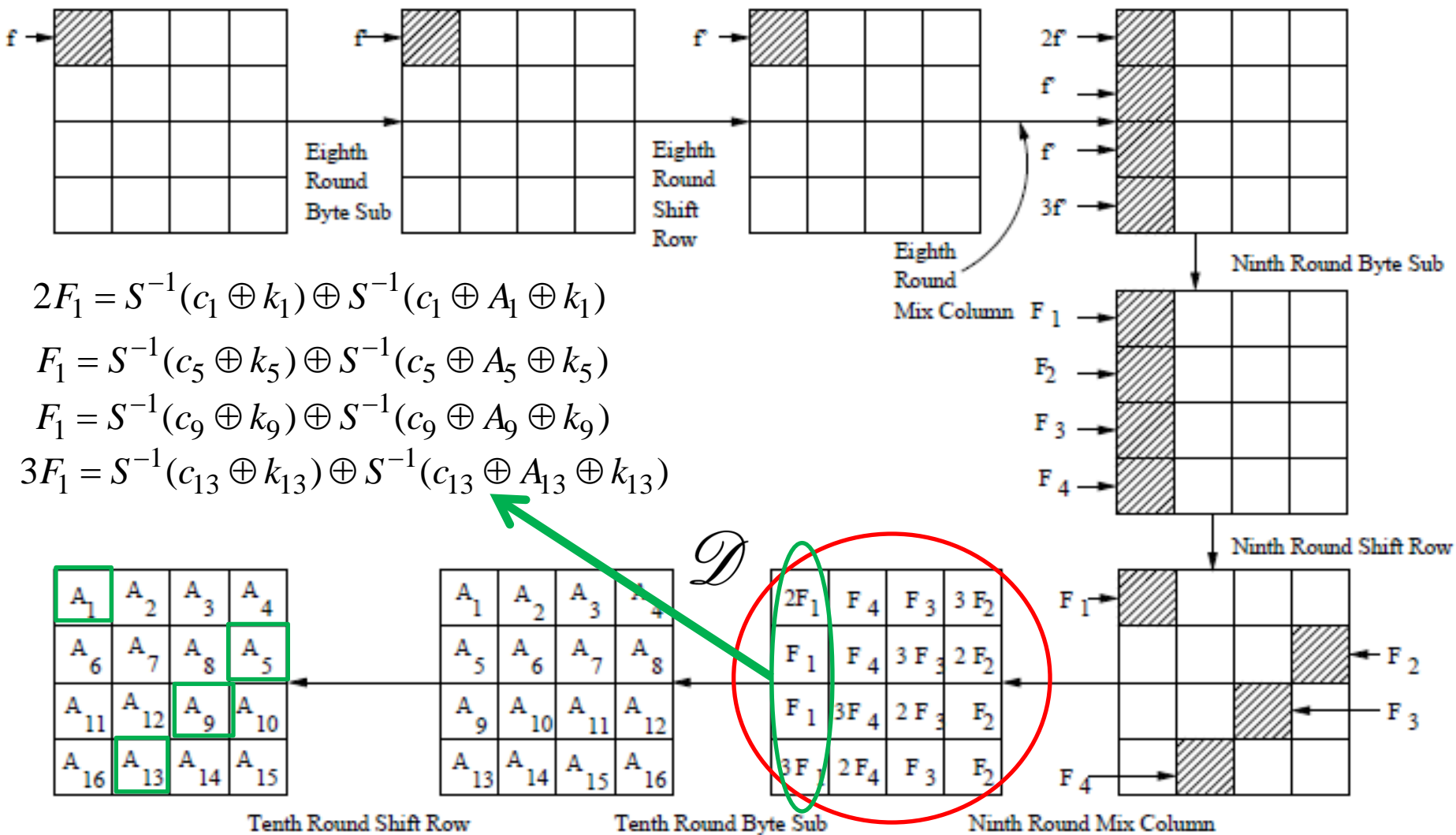


Some of the δ_j^i s are “Distinguishers”, But not all of them

Proposed Approach



Proposed Approach



Proposed Approach

- Formalization of DFA:
 - A DFA algorithm can be represented as follows:

$$\mathcal{A} = \langle \{\mathcal{D}_j^i\}, \mathcal{T}, \mathcal{R} \rangle$$

$\{\mathcal{D}_j^i\}$ A set of fault distinguishers, Constructed over the XOR differentials of a cipher state

\mathcal{T} An algorithm to evaluate the distinguisher over key guesses

\mathcal{R} Remaining key space, filtered with the distinguisher

Proposed Approach



Phase 1: Distinguisher from Fault Simulation Data?

Distinguishing Criteria: A state differential is a distinguisher iff state differential entropy is less than its maximum possible value.

Two Possible Cases

Case 1

Each w_z^{ij} is statistically independent



- Find variable ranges
- Reduced range → reduced entropy

Case 2

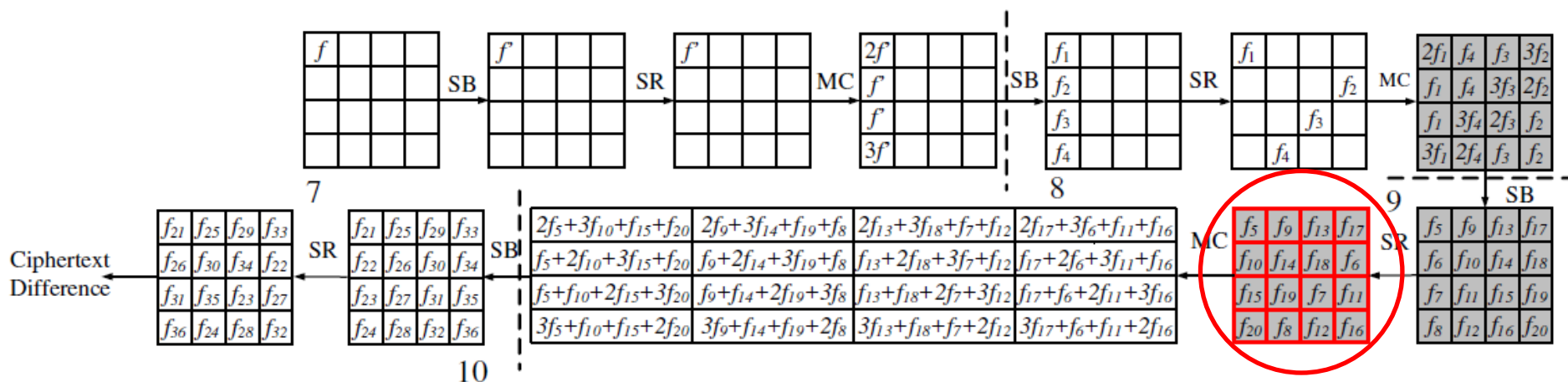
Some of the w_z^{ij} s are statistically dependent



- **Mine** variable dependencies
- Dependency → reduced entropy

Proposed Approach

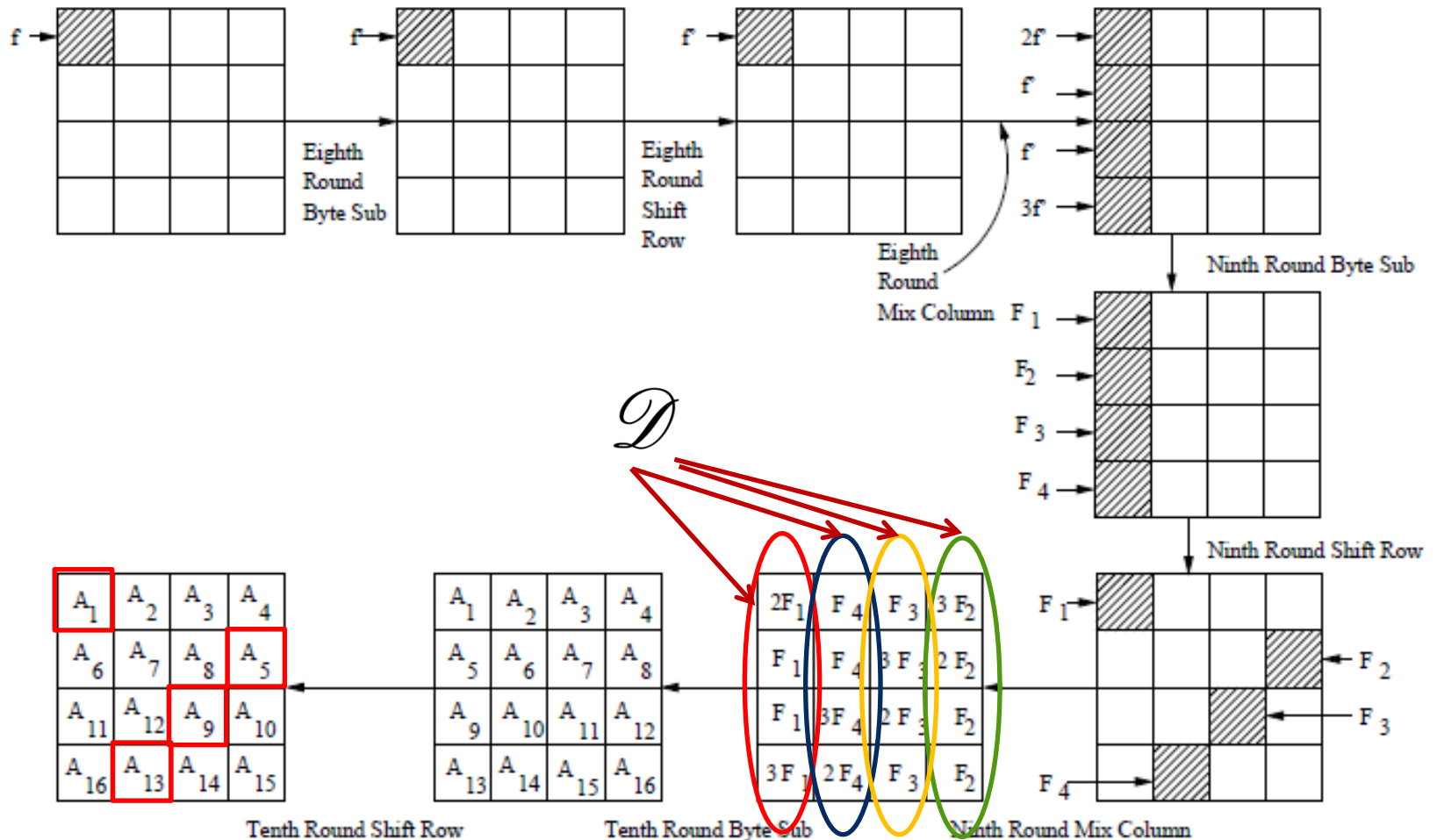
Example 1: Impossible Differential Distinguisher



$$H_{Ind}(\delta_9^4) = 127.90$$

Proposed Approach

Example 2: Distinguisher with Correlated Variables



Proposed Approach



Phase 2: Calculate Distinguisher Evaluation Complexity

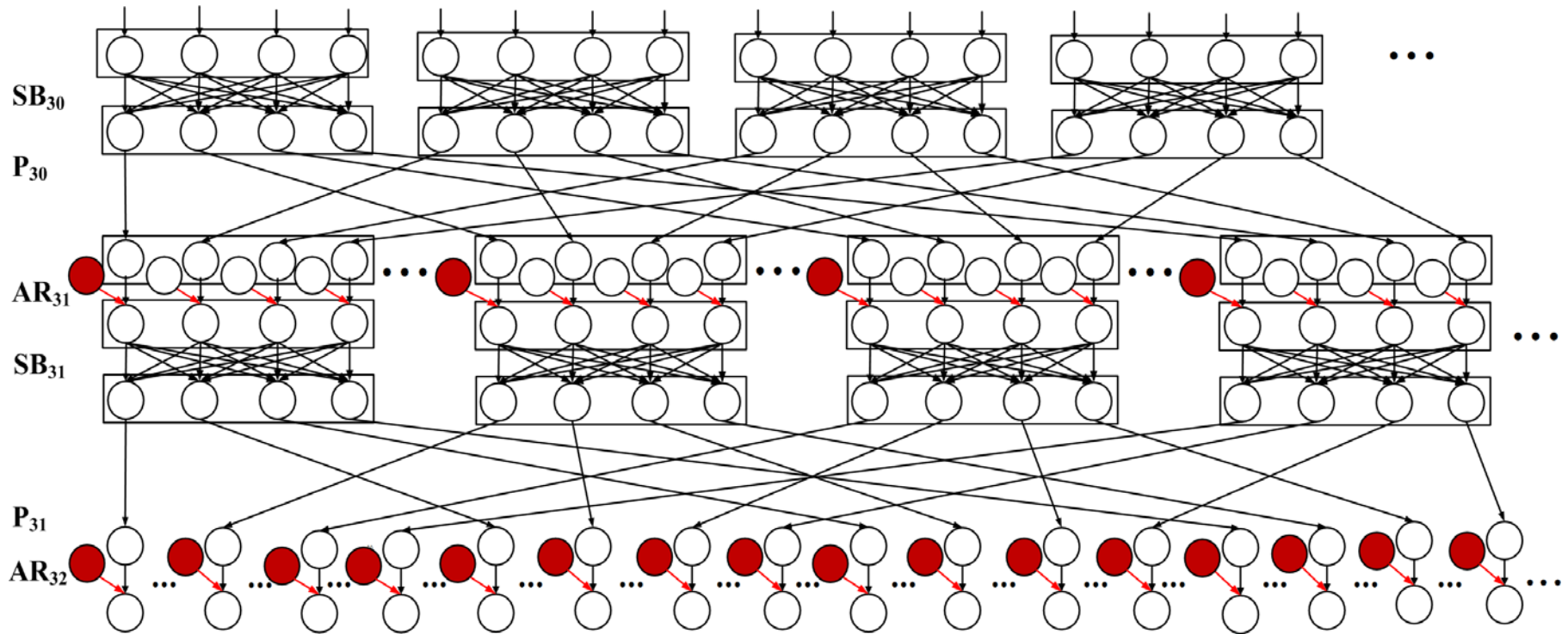
- Target: Evaluate a distinguisher
 - Identify the key bits to guess:
 - To evaluate each w_z^{ij}
 - To evaluate each variable set if exists.

A Graph Based Abstraction of the Cipher

- Cipher Dependency Graph (CDG)
 - Each node is a bit from any cipher state.
 - Directed links: Causal dependencies among bits

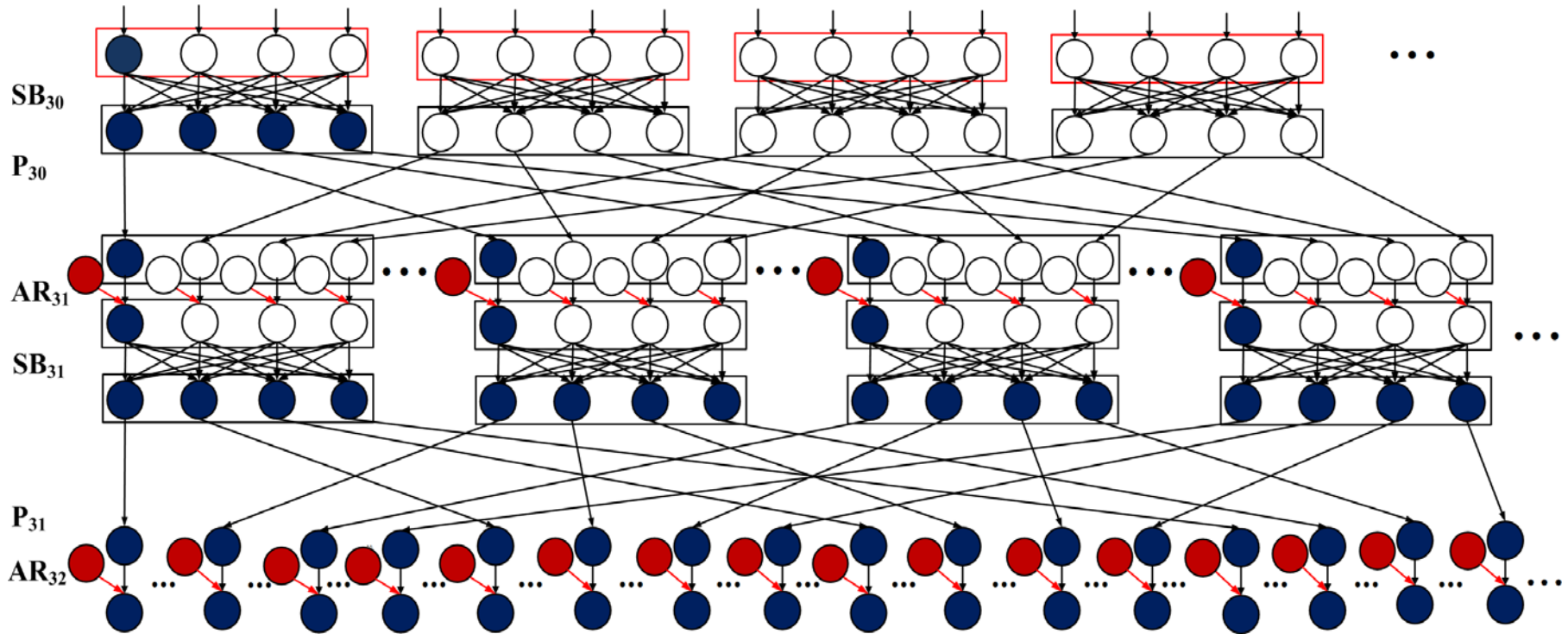
Proposed Approach

CDG: An Example



Proposed Approach

CDG: How Does it Work?



Proposed Approach

CDG: Maximum Independent Key Set and Variable Group

- Each (MKS_h, VG_h) pair represents an **independent subpart** of the distinguisher evaluation.
- Each Subpart can be evaluated in parallel.

$$T(h) = 2^{|MKS_h|}$$
$$\max_h(T(1), T(2), \dots, T(M))$$

Proposed Approach

Phase 3: Size of the Remaining Key Space

- Calculate the “probability of occurrence of the distinguishing property” with each VG_h

$$\mathcal{D}_j^i := \langle \{w_z^{ij}\}_{z=1}^l, \{Rng_{w_z^{ij}}\}_{z=1}^l, VS_{\delta_j^i}, \{IS_{\delta_j^i}^v\}_{v=1}^{|VS_{\delta_j^i}|} \rangle$$

for each VG_h

$$k_{size} := \text{BitCount}(MKS_h)$$

$$|\mathcal{R}|_{VG_h} := 2^{k_{size}} \times \mathbb{P}[VG_h]$$

$$|\mathcal{R}| := |\mathcal{R}| \times |\mathcal{R}|_{VG_h}$$

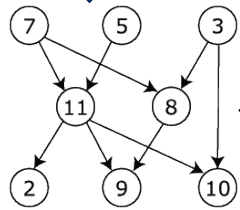
Calculated using these information

Proposed Approach: ExpFault



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Implementation Details

BEGINBLOCK SLAYER

OPTYPE NONLINEAR

OPINPUT 64

OPOUTPUT 64

% 0 = 0,1,2,3

% 1 = 0,1,2,3

% 2 = 0,1,2,3

% 3 = 0,1,2,3

% 4 = 4,5,6,7

% 5 = 4,5,6,7

% 6 = 4,5,6,7

% 7 = 4,5,6,7

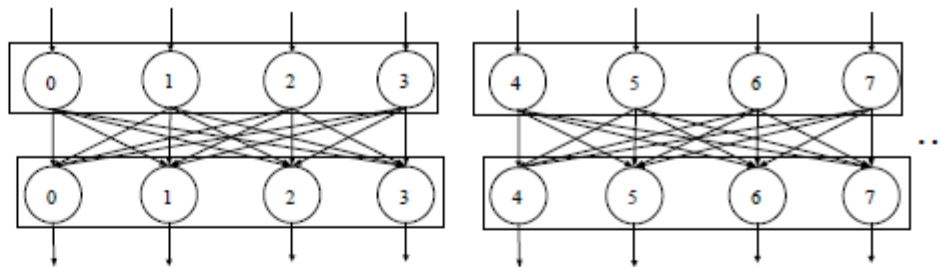
:

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ENDBLOCK SLAYER



Implementation Details

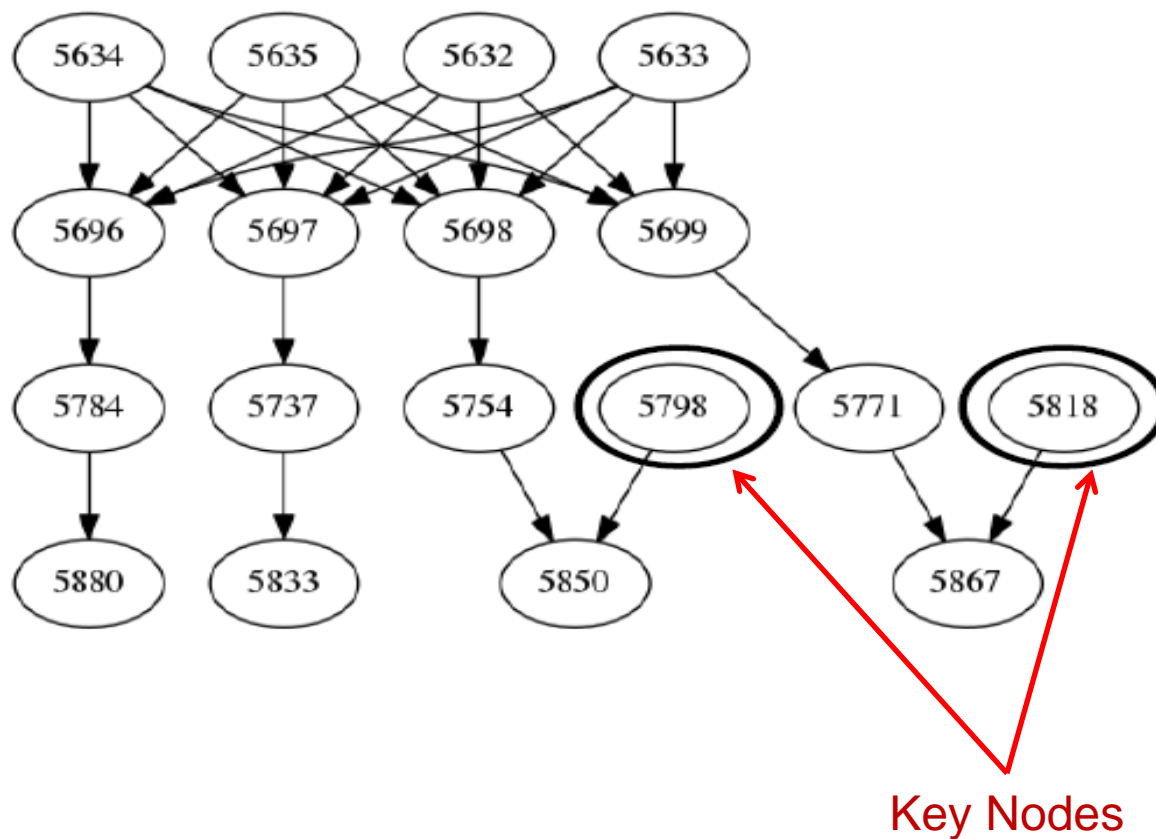
Distinguisher Evaluation Complexity (in log scale) 8
Remaining Key Space Complexity (in log scale) 11.53668207643374

Distinguisher Level 79
Round_no 27
Subop_no 2
Has_associations False
Entropy 43.536682076433735

V2 [0, 3, 5, 7, 9, 13,]
V0 [0, 3, 5, 7, 9, 13,]
V12 [0, 3, 7, 11, 15,]
V6 [0, 5, 6, 9, 10, 13, 14,]
V8 [0, 5, 6, 8, 9, 10, 11, 12, 15,]
V14 [0, 3, 7, 11, 15,]
V3 [0, 3, 5, 7, 9, 13,]
V1 [0, 3, 5, 7, 9, 13,]
V5 [0, 5, 6, 9, 10, 13, 14,]
V10 [0, 5, 6, 8, 9, 10, 11, 12, 15,]
V9 [0, 5, 6, 8, 9, 10, 11, 12, 15,]
V13 [0, 3, 7, 11, 15,]
V7 [0, 5, 6, 9, 10, 13, 14,]
V4 [0, 5, 6, 9, 10, 13, 14,]
V11 [0, 5, 6, 8, 9, 10, 11, 12, 15,]
V15 [0, 3, 7, 11, 15,]

No Variable sets exist..

How They Really Look Like?



Summary

- Characterization of the exploitable fault space for a block cipher is a problem of immense practical value.
- Exploitable fault space characterization demands **fast, generic and automated mechanism** for the characterization of individual fault instances.
- A fast automation is proposed
 - Need not to do the attack; just calculate the complexity
 - Best case attack complexity.
 - Number of injections
 - Attack description.
- Future works:
 - Further generalization – Key schedule attacks, DFIA attacks
 - Automatic generation of attack equations.
 - Synthesis of optimal countermeasures.
 - Countermeasure Vulnerability analysis.

Acknowledgements



Demo: ExpFault on AES

Acknowledgements



This project is funded by Synopsys CAD Labs under the project entitled “*Formal Methods for Physical Security Verification of Cryptographic Designs Against Fault Attacks*”

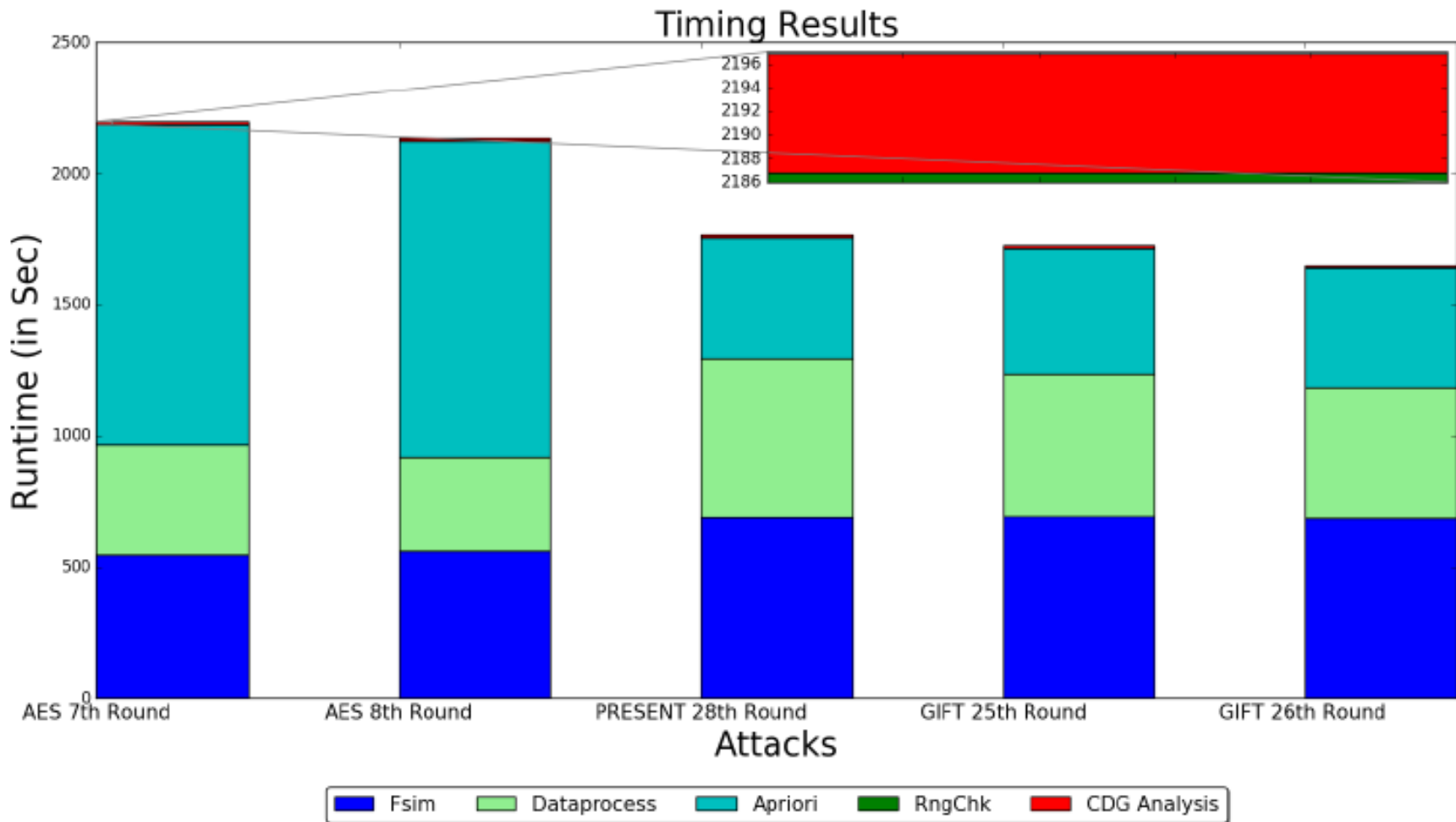


Thank You



Backup Slides

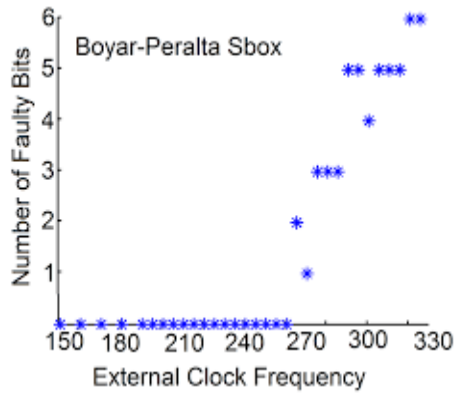
Implementation Details: Runtime



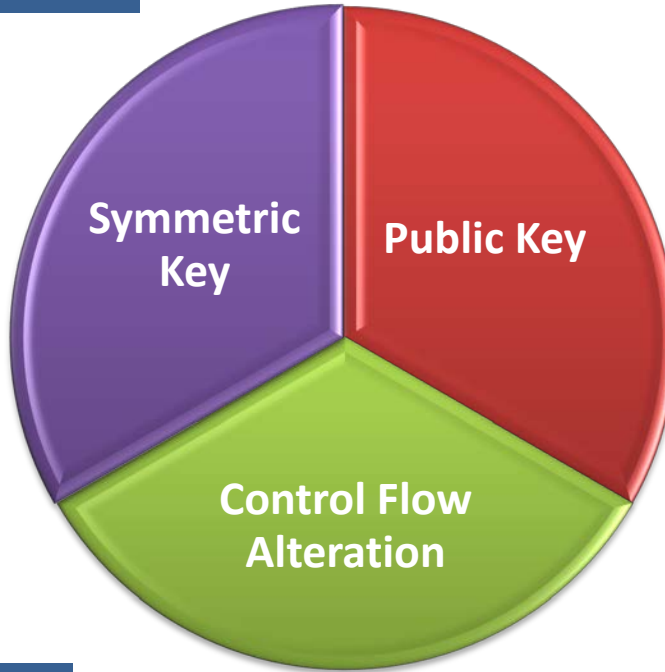
Introduction



Localized Random Faults



Biased Faults



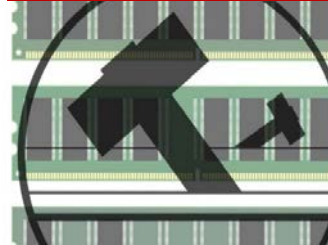
Laser-FI



EM-FI



Row-Hammer



Voltage-Glitch

Instruction Skip/Modify

```

1 LDD R24, Y+i // load subkey
2 LD R25, X // load state
3 EOR R24, R25 // ExclusiveOR
4 STD Z+i, R24 // store result
    
```

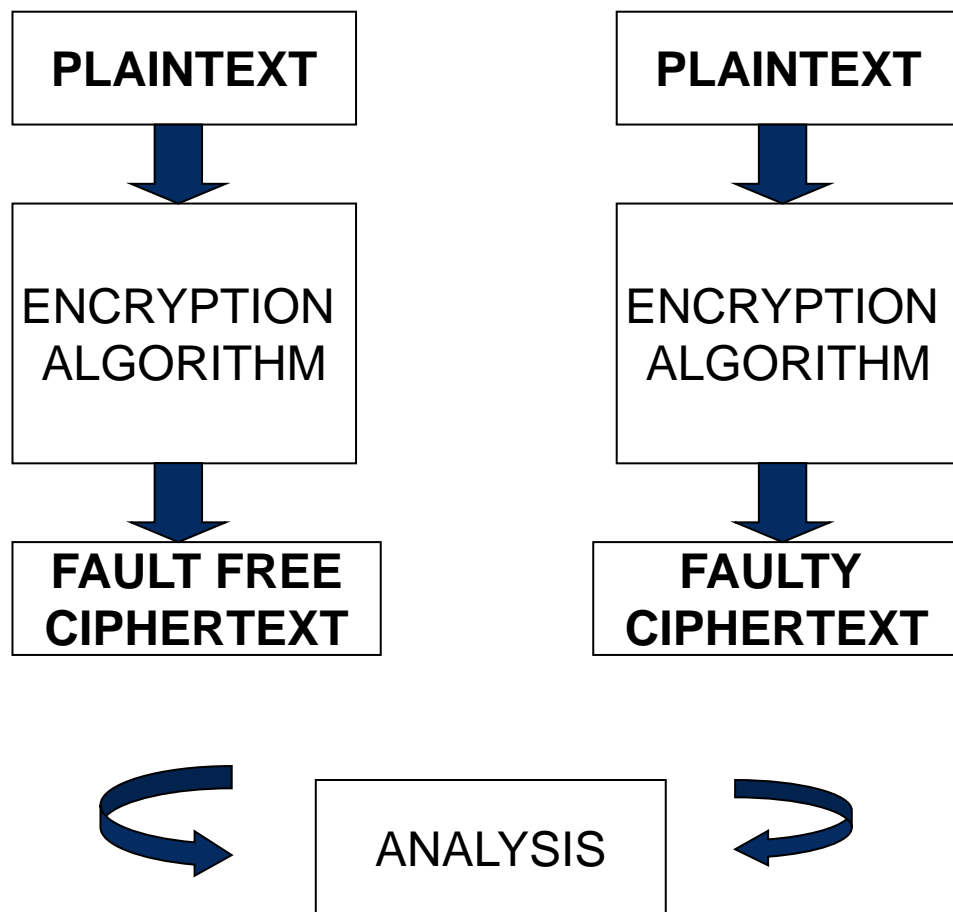
Clock-Glitch



Introduction



Differential Fault Analysis (DFA)



- Most widely explored
- Low fault complexity
- Analysis: complex
- Fault Locations
 - Datapath
 - Key-schedule
- Fault models
 - Bit based
 - Nibble based
 - Byte based
 - Multiple byte based

Motivation



Cipher Designer

Engineer